



GP 2111

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Matthew J. Adiletta et al.      Art Unit: 2111  
Serial No.: 10/053,172      Examiner: K. Kim  
Filed : January 17, 2002  
Assignee : Intel Corporation  
Title : FUNCTIONAL PIPELINES

**Mail Stop Amendment**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449. Also, enclosed are copies of the International Search Reports issued in related foreign counterpart applications.

Kindly accept this Information Disclosure Statement under Rule 97(c)(2). The rule 17(p) certification fee of \$180 is enclosed.

The documents are in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

November 24, 2004

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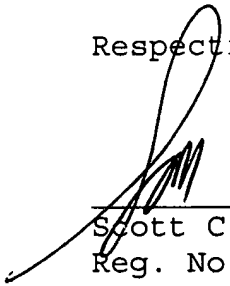
Attorney's Docket No.:10559-615001

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: November 24, 2004



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Substitute Form PTO-1449  
(Modified)U.S. Department of Commerce  
Patent and Trademark OfficeAttorney's Docket No.  
10559-615001Application No.  
10/053,172**Information Disclosure Statement  
by Applicant**

(Use several sheets if necessary)

(37 CFR §1.98(b))

Applicant  
Matthew J. Adiletta et al.Filing Date  
January 17, 2002Group Art Unit  
2111**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	09/473,571	N/A	Wolrich, et al.			12/28/1999
	AB	09/475,614	N/A	Wolrich, et al.			12/30/1999
	AC	5,913,059	06/1999	Torii			
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

**Foreign Patent Documents or Published Foreign Patent Applications**

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL	WO 01/16782	03/2001	WIPO				
	AM							
	AN							
	AO							
	AP							

**Other Documents (include Author, Title, Date, and Place of Publication)**

Examiner Initial	Desig. ID	Document
	AQ	Moors, et al., "Cascading Content-Addressable Memories", <i>IEEE Micro</i> , 12(3):56-66 (1992).
	AR	Shah, Niraj, "Understanding Network Processors", <i>Masters Thesis Submitted to the University of California, Berkeley</i> , September 4, 2001.
	AS	International Search Report from PCT/US03/01578; mailed February 19, 2004.
	AT	International Search Report from PCT/US03/01580; mailed April 21, 2004.

Examiner Signature

Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.